

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L3	3	lamothe-brian-p.in.	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:10	Inventor name search
2	BRS	L5	1	willet-david-s.in.	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:11	
3	BRS	L6	3	schwartzburg-irvin-j.in.	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:11	
4	BRS	L7	3	frederick-jesse-r.in.	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:12	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L9	3	daniel-measurement-and-control-inc.as.	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:12	
6	BRS	L10	86	US-10085298-\$.DID. OR US-10085298-\$.DID. OR US-0092005-\$.DID. OR US-6253193-\$.DID. OR US-6363488-\$.DID. OR US-6389402-\$.DID. OR US-6427140-\$.DID. OR US-5490216-\$.DID. OR US-5402492-\$.DID. OR US-5636280-\$.DID. OR US-6044154-\$.DID. OR US-5651064-\$.DID. OR US-5386369-\$.DID. OR US-5539828-\$.DID. OR US-5805712-\$.DID. OR US-6134324-\$.DID. OR US-6219677-\$.DID. OR US-6219677-\$.DID. OR US-6694318-\$.DID. OR US-6040788-\$.DID. OR US-6145103-\$.DID. OR US-5212774-\$.DID. OR US-5218362-\$.DID. OR US-6148242-\$.DID. OR US-6125446-\$.DID. OR US-5473692-\$.DID. OR US-5568552-\$.DID. OR US-6108420-\$.DID. OR US-5479612-\$.DID. OR US-6058478-\$.DID. OR US-6059838-\$.DID. OR US-6059838-\$.DID. OR US-6188995-\$.DID. OR US-4941175-\$.DID. OR US-6L191CG-\$.DID. OR	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:19	PLUS search

				US-6223288-\$.DID. OR US-6223288-\$.DID. OR US-4977594-\$.DID. OR US-5410598-\$.DID. OR US-5197332-\$.DID. OR US-5758068-\$.DID. OR US-5745569-\$.DID. OR US-6233567-\$.DID. OR US-5553143-\$.DID. OR US-5765152-\$.DID. OR US-6169976-\$.DID. OR US-5805699-\$.DID. OR US-4924378-\$.DID. OR US-5014234-\$.DID. OR US-5530752-\$.DID. OR US-6832349-\$.DID. OR US-5642480-\$.DID.			
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	L11	1143	"serial peripheral interface"	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:39	
8	BRS	L12	52	("serial peripheral interface" adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:38	Rev'd kwic/images
9	BRS	L15	43	l11 and (inter\$lintegrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:40	REv'd kwic/images
10	BRS	L16	5	"serial interface bus" and (inter\$lintegrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:51	Rev'd kwic/images

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
11	BRS	L18	3	"MPC862SR"	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:52	Rev'd kwic/images
12	BRS	L21	12	l10 and (("non-volatile" or ROM or EE\$PROM or flashrom) with microcontroller\$1)	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 10:03	Rev'd kwic/images
13	BRS	L23	5	l21 and licens\$3	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 10:03	Rev'd kwic/images
14	BRS	L24	201	"705"/\$.ccls. and (("non-volatile" or ROM or EE\$PROM or flashrom) with microcontroller\$1)	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 10:04	Rev'd images

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
11	BRS	L18	3	"MPC862SR"	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 08:52	Rev'd kwic/images
12	BRS	L21	12	l10 and (("non-volatile" or ROM or EE\$PROM or flashrom) with microcontroller\$1)	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 10:03	Rev'd kwic/images
13	BRS	L23	5	l21 and licens\$3	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 10:03	Rev'd kwic/images
14	BRS	L24	201	"705"/\$.ccls. and (("non-volatile" or ROM or EE\$PROM or flashrom) with microcontroller\$1)	US-PGPUB; USPAT; EPO; JPO; DERWE NT; IBM_T DB	2005/10/26 10:04	Rev'd images

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
15	BRS	L29	1	713/189.ccls. and (non\$1volatile with (micro\$1controller\$1 or micro\$1processor\$1) with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 11:46	REv'd kwic/im ages
16	BRS	L30	0	713/192.ccls. and (non\$1volatile with (micro\$1controller\$1 or micro\$1processor\$1) with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 11:46	
17	BRS	L31	0	713/193.ccls. and (non\$1volatile with (micro\$1controller\$1 or micro\$1processor\$1) with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 11:46	
18	BRS	L32	0	"235"/\$.ccls. and (non\$1volatile with (micro\$1controller\$1 or micro\$1processor\$1) with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 11:46	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	L33	2	"705"/\$.cccls. and (non\$1volatile with (micro\$1controller\$1 or micro\$1processor\$1) with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 13:37	Rev'd kwic/im ages
20	BRS	L34	1	"713"/\$.cccls. and (non\$1volatile with (micro\$1controller\$1 or micro\$1processor\$1) with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 12:45	Rev'd kwic/im ages
21	BRS	L35	0	"726"/\$.cccls. and (non\$1volatile with (micro\$1controller\$1 or micro\$1processor\$1) with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 12:45	
22	BRS	L36	30	"713"/\$.cccls. and (non\$1volatile with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 13:13	REv'd kwic/im ages

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
23	BRS	L37	6	110 and (non\$1volatile with licens\$3)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 13:13	REv'd kwic/im ages
24	BRS	L38	2	"5805802".pn.	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT; IBM_T DB	2005/10/2 6 13:37	